

**FOR IMMEDIATE RELEASE**

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**CYPRESS, SYNPLICITY OFFER PROMOTIONAL SYNPLIFY® SOFTWARE  
FOR Ultra37000™ CPLDs**

**Enables Efficient Design Flow Between Synplicity® Tools and *Warp*™ Software**

SAN JOSE, Calif., March 1, 2000 -- Cypress Semiconductor Corp. (NYSE:CY) and Synplicity, Inc. today announced an exclusive offer for users of Cypress's *Warp*™ development tools. Qualified users can receive an evaluation copy of Synplicity's Synplify® Version 5.3.1 VHDL and Verilog synthesis solution. The software is dedicated to Cypress's Ultra37000™ and FLASH370i™ CPLDs, and is valid for 90 days.

Users who take advantage of the offer will download the Cypress Synplicity® Bolt-in Kit. The kit provides an easy-to-use way to enable an efficient design flow between the Synplify environment and Cypress's popular *Warp*™ software. It gives users access to Cypress's high-performance silicon in the popular Synplicity design environment.

The Cypress Synplicity Bolt-in Kit allows customers to enter designs in the Synplify environment, and use Synplicity's Behavior Extracting Synthesis Technology™ (B.E.S.T.™) to achieve optimum results. The resulting netlist can then be passed to Cypress's *Warp* software, where users can benefit from fitting and mapping technology developed specifically for Cypress CPLDs.

"This is a great opportunity for designers to take a no-risk look at the Synplicity tools and Cypress's CPLDs," said Norman Taffe, director of marketing for programmable logic at Cypress. "We are confident that they will like what they see."

"We are pleased to extend this offer to users that may not have had a chance to use Synplify in the past," said Joe Gianelli, Channel Marketing Director at Synplicity. "We have received a lot of positive feedback from users of the Cypress Synplicity Bolt-in Kit, and wanted to expose more engineers to this solution."

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For more information and to sign-up for the offer, users are encouraged to visit <http://www.cypress.com/pld/synplicity>. The evaluation copy of Synplify Version 5.3.1 and Cypress's Synplicity Bolt-in Kit are available immediately.

#### **Broad Programmable Logic Offering**

Cypress offers a broad range of programmable logic, including industry-standard SPLDs such as the 16V8, 20V8 and 22V10, and the In-System Reprogrammable™ (ISR™) Ultra37000 and FLASH370i CPLDs. Cypress's upcoming Delta39K™ CPLDs span from 50,000-to-350,000 gates, many times larger than any other CPLD, and are the first CPLDs to offer embedded memory elements, including optimized FIFO and dual-port memory blocks. Cypress's devices are supported by the *Warp* design tools, giving users access to a broad range of devices with a single, open development tool.

#### **About Synplicity**

Founded in 1994, Synplicity, Inc. delivers the benefits of logic synthesis and embedded synthesis technologies to programmable logic and ASIC designers by developing fast, easy-to-use, affordable tools with extremely high quality of results. Synplicity products support industry-standard design languages (VHDL and Verilog), run on popular platforms (Windows 95/98, Windows NT and UNIX) and support leading PLD manufacturers. Synplicity is a Platinum member of the Cadence Connections Program (NYSE: CDN), Synopsys Liberty Program (Nasdaq: SNPS), and other industry standards organizations. The company is located at 935 Stewart Drive, Sunnyvale, Calif. 94086. Telephone: 408/215-6000; Fax: 408/990-0290; E-Mail: [info@synplicity.com](mailto:info@synplicity.com).

#### **About Cypress**

Cypress Semiconductor provides high-performance integrated circuit solutions "By Engineers. For Engineers.™" for fast-growing companies in fast-growing markets, including data communications, telecommunications, computation, consumer products, and industrial-control. With a focus on emerging communications applications, Cypress's product lines include networking-optimized and micropower static RAMs; high-bandwidth multi-port and FIFO memories; high-density programmable logic devices; timing technology for PCs and other digital systems; and controllers for Universal Serial Bus (USB). Cypress is No. 1 in the USB and clock chip markets.

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More than two-thirds of Cypress's sales come from fast-growing datacom/telecom markets and dynamic companies such as 3Com, Alcatel, Cisco, Ericsson, Lucent, Motorola, and Nortel Networks. Cypress's ability to mix and match its broad portfolio of intellectual property enables targeted, integrated solutions for high-speed systems that feed bandwidth-hungry Internet applications. Cypress aims to become the preferred silicon supplier for Internet switching systems and for every Internet data stream to pass through at least one Cypress IC.

Cypress employs more than 3,600 people worldwide with international headquarters in San Jose, California. Its shares are listed on the New York Stock Exchange under the symbol CY. More information about Cypress is accessible electronically on the company's worldwide web site at <http://www.cypress.com> or by CD-ROM (call 1-800-858-1810). An electronic investor forum, and other investor information, is located at <http://www.cypress.com/investor/index.html>.

"Safe Harbor" Statement under the Private Securities Litigation Reform Act of 1995: Statements herein that are not historical facts are "forward-looking statements" involving risks and uncertainties. Please refer to Cypress's Securities and Exchange Commission filings for a discussion of such risks.

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Ultra37000, FLASH370i, *Warp*, In-System Reprogrammable, ISR, and Delta39K are trademarks of Cypress Semiconductor Corp. Synplicity and Synplify are registered trademarks. Behavior Extracting Synthesis Technology, and B.E.S.T. are trademarks of Synplicity, Inc.